

ABSTRACT OF THE DISCLOSURE

An encryption control apparatus for encrypting and decrypting data that ensures data security by preventing illegal deciphering or leakage of the data is provided. A CPU 11A for running a program, a ROM 13A for storing the program run by the CPU, a RAM 12A used as a work area while the CPU is running the program, an I/O section 14A for sending/receiving data to/from an external device, and an encryption section 15A for decrypting encrypted data and encrypting plain text data are formed on a single semiconductor device.